

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
BEFORE THE BOARD OF PATENT APPEALS AND INTERFERENCES

# 45  
6/2/2000  
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In re the Application of:

✓ KIRA, et al.

✓ Serial Number: 08/897,953

✓ Filed: July 24, 1997

✓ For: METHOD AND SYSTEM FOR FABRICATING SEMICONDUCTOR DEVICE



Group Art Unit: 2814

Examiner: D. Graybill

**NOTICE OF APPEAL**

Director of Patents and Trademarks  
Washington, D.C. 20231

Date: May 30, 2000

Sir:

Applicant(s) hereby appeal(s) to the Board of Patent Appeals and Interferences from the decision dated November 30, 1999 of the Primary Examiner.

The item(s) checked below are appropriate:

1. ☒ A petition for a third month extension of time to respond to the Final Rejection filed herewith.
2. ☒ A timely response to the Final Rejection has been filed, as provided in 37 C.F.R. 1.113.
3. ☒ Fee -\$300.00 or ☐ Small Entity -\$150.00

- ☒ Enclosed (This paper is filed in triplicate.)  
☐ Not required (fee paid in prior appeal)  
☐ Charge to Deposit Account No. 01-2340 (This paper is filed in duplicate.)

In the event this Notice of Appeal is not considered to be timely filed, applicant(s) hereby petition(s) for an appropriate extension of time. The fee for this extension may be charged to our Deposit Account No. 01-2340, along with any other additional fees which may be required with respect to this Notice.

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Respectfully submitted,  
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